

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:

Shunpei YAMAZAKI et al.

Serial No. 09/535,015

Filed: March 24, 2000

For: METHOD OF MANUFACTURING

A SEMICONDUCTOR DEVICE )

Art Unit: 2825

Examiner: F. Abraham

2825  
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SEP -6 2001

TC 2800 MAIL ROOM

#12  
Prior Art

FJONES

10-30-02

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents and Trademarks

Washington, D.C. 20231

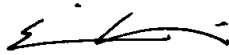
Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application.

The references listed on the attached Form PTO-1449 were previously cited in co-pending application Serial No. 09/428,471. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

  
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